

WEST Search History

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DATE: Monday, June 28, 2004

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		<i>DB=JPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L10	clock and replica adj2 delay	1
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L9	L8 and replica adj2 delay	36
<input type="checkbox"/>	L8	L7 and clock adj3 signal\$	426
<input type="checkbox"/>	L7	L6 and clock\$ adj5 phase\$	429
<input type="checkbox"/>	L6	L3 and (produc\$ or generat\$) adj3 clock\$	696
<input type="checkbox"/>	L5	L3 and (produc\$ or generat\$) adj3 clock\$	696
<input type="checkbox"/>	L4	L3 and (produc\$ or generat\$) adj3 clock	691
<input type="checkbox"/>	L3	L2 and variable delay	1156
<input type="checkbox"/>	L2	L1 and semiconductor	146783
<input type="checkbox"/>	L1	memory	624736

END OF SEARCH HISTORY

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